

### **AMENDMENTS TO THE SPECIFICATION**

*Please replace paragraph [0026] with the following amended paragraph:*

In one embodiment, the MMX unit 206 also includes two carry-save adders (not shown) that reduce the sum and carry from adder 328A and sum and carry from adder 328B to a single sum and carry that are provided to full adder 332, which generates the single final sum 108. This embodiment is illustrated in Figure 5.

*Please add the following new paragraph [0011.1] after paragraph [0011]:*

FIGURE 5 is a block diagram illustrating the MMX unit of Figure 2 according to an alternate embodiment of the present invention.

*Please add the following new paragraph [0038.1] after paragraph [0038]:*

Referring now to Figure 5, a block diagram illustrating the MMX unit 206 of Figure 2 according to an alternate embodiment of the present invention is shown. The MMX unit 206 of Figure 5 is similar to the embodiment of Figure 3 and corresponding elements are numbered alike. However, the MMX unit 206 of Figure 5 also includes a carry-save adder 502 that receives a sum output 512A and carry output 514A of multiplier A carry-save adder 328A and a sum output 512B of multiplier B carry-save adder 328B, which the carry-save adder 502 adds to generate a sum 516 and a carry 518. Additionally, the MMX unit 206 of Figure 5 also includes a carry-save adder 504 that receives a carry output 514B of multiplier B carry-save adder 328B and sum output 516 and carry output 518 of carry-save adder 502, which the carry-save adder 504 adds to generate a sum 522 and a carry 524. The full adder 332 receives the sum 522 and carry 524 generated by carry-save adder 504, which full adder 332 adds to generate the result 108.